

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
	)	
Hiroya KOBAYASHI, et al.	)	Confirmation No.: 9269
	)	
Application No.: 10/573,467	)	Group Art Unit: 2822
	)	
Filed: April 13, 2007	)	Examiner: Paul E. Patton
	)	
For: SEMICONDUCTOR DEVICE AND	)	
METHOD FOR MANUFACTURING	)	
THE SAME	)	

Commissioner for Patents  
U.S. Patent and Trademark Office  
**Customer Window, Mail Stop RCE**  
Alexandria, VA 22314

Sir:

**PRELIMINARY AMENDMENT IN RCE**

Concurrently filed herein with a Request for Continued Examination and prior to continued examination, please amend the above-identified application as follows. This filing is in response to the Advisory Action dated September 21, 2009, the Final Office Action dated December 9, 2008, and the Notice of Appeal filed on June 9, 2009 the period for response to which extends through October 9, 2009 by the concurrently-filed petition for a two-month extension of time and corresponding fee payment.